Dr Mohammed Abdul Raheem

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Expertise

Telecommunications

Analog and Mixed Signal in VLSI

Work experience

1. Muffakham Jah College of Engineering and Technology 2010 — Present

   Assistant Professor
   Hyderabad

Education

1. P.hD - 2020

   GITAM University Hyderabad

Honours and Awards

1. Silver Medal Best Paper award - 2015

   IEEE PRIMEASIA2015

Membership In Professional Bodies

1. IEEE (CAS/EDS) 92171164, 2010
   Professional Senior Member Hyderabad Section

Membership In Committees

Patent

1. Design
   Inventor(s): Arshad Mohammed A S L K Gopalamma M Nalini Manasa M A RAHEEM, Assignee: MJCET
   Published: 2023-01-10

2. Prototype for Detection and Classification of Pancreatic Tumor using CNN feature-based LLRBFNN Model
   Inventor(s): Dr Mohammed Abdul Raheem Dr MAHALTHY MOHAMMED SABIR HUSSAIN Mohammed Noorullah Khan Mr.Hakeem Aejaz Aslam, Assignee: Muffakham Jah College of Engineering and Technology
   Published: 2023-03-17

Publication

1. Fetal Health Prediction using neural networks

   Ayanesh Chowdhury ., Ankit Chahar ., Rohan Eswara ., Mohammed Abdul Raheem ., Shaik Ehetesham ., Bharath Kumar Thulasidoss .,
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2. IEEE 754 Floating Point Pipelined Multiplier with Karatsuba for Mitigations of Area and Power

Mohammed Abdul Raheem ., Mohammed Abdul Rahman Shareef .,
Lecture Notes in Networks and Systems, Volume 147, Year 2021, Pages 3-7

3. ## AI Algorithm System for Prediction of Diabetes Using Progressive Web Appand IBM Cloud

   Mohammed Abdul Raheem1 , Shaik Ehetesham2 , Mohammad Faiz Ahmed Subhani3 , Sayed Abdul Zakir
   International Journal of Science and Research (IJSR), Volume Volume 10 Issue 5, May 2021, Year 2021, Pages 638

4. ## A Deep Learning Approach for the Automatic Analysis and Prediction of Breast Cancer for Histopathological Images using A Webap

   Mohammed Abdul Raheem , Shaik Tabassum , Syeda Kulsoom Nahid , Syeda Areej Anzer
   NTERNATIONAL JOURNAL OF ENGINEERING RESEARCH & TECHNOLOGY (IJERT), Volume Volume 10, Issue 06 (June 2021), Year 2021, Pages

5. ## SIC-TPG for path delay fault detection in VLSI circuits using scan insertion method

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6. ## A Low Voltage NMOS current bleeding down conversion Mixer with source degeneration in 0.18µm CMOS Technology

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   Proceedings of 4th International Conference on 2021 Devices for Integrated Circuit, DevIC 2021, Volume , Year 2021, Pages 637-642

7. ## A Low Voltage NMOS current bleeding down conversion Mixer with source degeneration in 0.18 μm CMOS Technology

   Raheem, MA and Hussain, M Mohamed Sabir
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8. ## A Bio Potential Sensor Circuit of AFE Design with CT ∑-Δ Modulator

   M. A. Raheem ., K. Manjunathachari .,
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9. ## A 1.2V, 0.7mW of Low Noise Amplifier for WSN Band

   Mohammed Abdul Raheem ., Mohammed Arifuddin Sohel ., Maryam Fatima .,
   Proceedings of the 5th International Conference on Inventive Computation Technologies, ICICT 2020, Volume , Year 2020, Pages 864-867

10. ## A 1.2 V, 0.7 mW of Low Noise Amplifier for WSN Band

    Raheem, Mohammed Abdul and Sohel, Mohammed Arifuddin and Fatima, Maryam
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11. ## A Design Bio Potential with CT Sensor∑-Circuit of AFE Modulator

    Raheem, MA and Manjunathachari, K
    New Trends in Computational Vision and Bio-inspired Computing: Selected works presented at the

ICCVBIC 2018, Coimbatore, India, Year 2020, Pages 305

12. ## A Bio Potential Sensor Circuit of AFE Design with CT ¯-Δ Modulator

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    New Trends in Computational Vision and Bio-Inspired Computing - Selected Works Presented at the ICCVBIC 2018, Volume , Year 2020, Pages 305-312

13. ## DESIGN & ANALYSIS OF 2 -PHASE OP -AMP FOR LDO APPLICATION

    M.A.Raheem, K.Manjunathaachari
    International Journal of Pure and Applied Mathematics, Volume , Year 2018, Pages

14. ## IoT based Oxygen Monitoring System for Health Care Management in Hospitals

    MA Raheem; K. Manjunatha Chari;
    Journal of Advanced Research in Dynamical and Control Systems , Volume Issue 07, Year 2018, Pages

15. ## A Two Channel Analog Front end Design AFE Design with Continuous Time Σ-Δ Modulator for ECG Signal

    M. A. Raheem, K. Manjunathachari
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16. ## A 0.5 V 30.78 μW 78dB DR Using 2Nd Order Sigma-Delta Modulator for Biomedical Application

    Fathima, Syeda and Raheem, Mohammed Abdul and Manjunathachari, K
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17. ## A Two Channel Analog Front end Design AFE Design with Continuous Time Σ-Δ Modulator for ECG Signal

    Raheem, Mohammed Abdul and Manjunathachari, K
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18. ## Design \& Analysis of 2-phase Op-Amp for LDO Application

    M.A.Raheem, K.Manjunathachari
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19. ## IoT based Oxygen Monitoring System for Health Care Management in Hospitals

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20. ## A Bio Potential Sensor Circuit of AFE Design With CT Σ-Δ Modulator

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23. A 0.5V 30.78µW 78dB DR using 2Nd order sigma-delta modulator for biomedical application

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24. Design of discrete time notch filter for biomedical applications

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25. Quad Mode of Sixteen-Channel Chopper AFE Design and Cascaded with Continuous Time ∑-Δ Modulator for Electro-Encephalogy Monitoring System

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26. Quad Mode of Sixteen-Channel Chopper AFE Design and Cascaded with Continuous Time Σ-Δ Modulator for Electro-Encephalogy Monitoring System.

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28. Quad mode of sixteen-channel chopper AFE design and cascaded with continuous time Σ-Δ modulator for electro-encephalogy monitoring system

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29. A Fully Differential Gain Boosted Floded Cascode OTA with 100dB Gain

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31. ## A design of 2nd order DT sigma-delta modulator for medical implants

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33. ## A novel concurrent BIST architecture for register based recording and windowed playback

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34. ## A high-speed reversible low-power Error Tolerant Adder

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Asia Pacific Conference on Postgraduate Research in Microelectronics and Electronics, Volume , Year 2012, Pages 178-183

35. ## A MITIGATION TECHNIQUE FOR SRAM BASED FPGA

Omer, Sadaf and Raheem, MA and Yasmeen, Syeda and Firdous Naziya