# MUFFAKHAM JAH COLLEGE OF ENGINEERING AND TECHNOLOGY Course outcomes of I and II semester

#### I SEMESTER

s.	Course	Course Name	COs Code	COs
No	Code		EG 106.1	Recognize the role of communication and clarify its different forms.
			EG 106.2	Write grammatically correct English.
1	MC 106 EG	Engineering English	EG 106.3	Write efficiently for a variety of professional and social situations using appropriate writing styles.
			EG 106.4	Enrich their vocabulary.
			EG 106.5	Comprehend texts efficiently
			MT 101.1	Examine the convergence and divergence of an infinite series by various methods like P test, Ratio test, and Root test.
			MT 101.2	Using limits, continuity and differentiability of a single variable to classify the mean value theorems and calculate Radius of curvature, envelope, evolutes and trace the curves.
			MT 101.3	Use partial derivatives to calculate maxima and minima of function of several variables and multiple integrals.
2	BS 101 MT	Engineering Mathematics - I	MT 101.4	Compute line, surface, volume integrals and illustrate Green's, Gauss, and Stokes theorems.
			MT 101.5	Perform row and column operation to find rank and hence obtain the solution of system of simultaneous linear equations, Basis and dimension of vector spaces.
			MT 101.6	Apply Cayley Hamilton Theorem to find the inverse and powers of the Matrix.and converting Quadratic equation to canonical form.
			PH 102.1	To apply the principles of Optics to calculate the wavelength of monochromatic and polychromatic light.
			PH 102.2	Using basic principles of statistical mechanics students can able to understand the materials and their properties.
3	BS 102 PH	Engineering Physics -I	PH 102.3	Ability to understand the basic characteristics and design the semiconductor devices by using band theory of solids.
			PH 102.4	Ability to understand the arrangement of atoms and predict the defects in the crystals.
			PH 102.5	Ability to identify, distinguish and characterize the matrials and their properties related to Dielectric, Magnetic, Nano and Superconductors.
			CH 103.1	Apply the concept of electrode potential in identifying feasibility of electrochemical reaction; illustrate electro analytical techniques and working of batteries.
			СН 103.2	Identify the mechanism of corrosion of materials on basis of electrochemical approach and devise corrosion control methods.
4	BS 103 CH	Engineering Chemistry -I	CH 103.3	Estimate the physical & chemical parameters of quality of water and explain the process of water treatment.
			CH 103.4	Explain the influence of chemical structure on properties of materials and their <b>choice</b> in engineering applications
			CH 103.5	Relate the concept of green chemistry for <b>design</b> of and manufacturing of engineering materials.
			СН 103.6	Classify chemical fuels and grade them through qualitative analysis.

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Т				Identify the basic components of computer and outline the domain of
1			CS 105.1	problem solving
-1			CS 105.2	Demonstrate the applications of structured data
- 1			CS 105.3	Incorporate the concepts of modular programming, explicit memory
5	ES 105 CS	Computer Programing and P		management & Preprocessor into C programs  Practice to organize information using several processes and store it
- 1			CS 105.4	permanently
- 1				Apply the basic features of object oriented programming in order to
- 1			CS 105.5	strengthen problem solving
_			CF +0+ +	Analyze the effect of a coplanar and non- coplanar system of forces on a
- 1			CE 104.1	body.
			CE 104.2	Analyze the static equilibrium of bodies in 2D and 3D.
- 1			CE 104.3	Analyze the trusses & frames using different methods.
اء	rr 404 or	PROVINCES AND	CE 104.4	Determine the effect of friction and its governing laws on simple and
٩	ES 104 CE	Engineering Mechanics -I		connected systems.  Determine the Centroid of lines, areas and volumes of revolution.
- 1			CE 104.5	Compute the Area Moment f Inertia & Product of Inertia of different
- 1			200 000 0000	areas.
- 1			CE 104.6	Compute the Area Moment of Inertia & Product of Inertia of different
			CE 104.6	areas.
			CE 153.1	Understand Basics of Engg drawing, simple shapes and basic commands
- 1				of auto cad.
			CE 153.2	Draw the construction of Scales, Understand and describe the concepts projections
7	ES 153 CE	Engineering Graphics Lab -I	CE 153.3	Draw the projection of straight lines and its traces
- 1				construction of perpendicular planes, Oblique planes and Traces of
			CE 153.4	planes.
			CE 153.5	construction of solids with axis inclined to one or both the reference
_			CC 255.5	planes
			1	Conduct experiments, take measurements and analyze the data throug
			PH 132.1	hands-on experience in order to demonstrate understanding of the
				theoretical concepts of applied physics, while working in small groups.
			PH 132.2	Demonstrate writing skills through clear laboratory reports.
		Engineering Physics	DU 1222	Interpret the principles of optics and determine the properties of
8	BS 151 PH	Engineering Physics Laboratory	PH 132.3	materials graphically
		Laudiatory		Compare the experimental results with those introduced in lecture, dra
			PH 132.4	relevant conclusions and substantiate them satisfactorily.
			PH 132.5	Transfer group experience to individual performance of experiments ar
				demonstrate effective oral communication skills.
			CH 152.1	Apply the concept of electrode potential in identifying feasibility of
			24 2422	electrochemical reaction;
			CH 152.2	Illustrate electro analytical techniques and working of batteries
			CH 152.3	Identify the mechanism of corrosion of materials on basis of
٥	BS 152 CH	Engineering Chemistry		electrochemical approach and devise corrosion control methods.  Estimate the physical & chemical parameters of quality of water and
-	03 13L C.I	Laboratory	CH 152.4	explain the process of water treatment.
			**************************************	Explain the influence of chemical structure on properties of materials a
			CH 152.5	their choice in engineering applications
			CH 152.6	Classify chemical fuels and grade them through qualitative analysis
			ME 155.1	Use proper tools and prepare the work piece in wooden material.
			ME 155.2	Use proper tools for marking, measuring, cutting and finishing the work
10	ES 155 MF	Engineering Workshop -I	ME 155.3	piece on metal plate.  Prepare various types of joints by Arc welding process
		G Harring I		
			ME 155.4	Use suitable tools to make plumbing joint assembly for water supply.
			ME 155.5	Prepare layout of electrical circuits and testing the circuits.
			CS 154.1	Apply the design concepts for development of process and interpret da
11	EC 1EACC	Computer Programming	CS 154.2	Demonstrate knowledge of programming environment, compiling,
11	ES 154 CS	Laboratory		debugging, linking and executing variety of programs  Demonstrate documentation and presentation of the algorithms /
			CS 154.3	flowcharts / programs in a record form
			CS 154.4	Validate the process using known input-output parameters
			MC 156.1	Aquire listening competence
		Engineering English	MC 156.2	Enhance speaking skills through Phonetics and Interactive activities
12	MC 156 EG	Engineering English Laboratory	MC 156.3	Improve reading & PPT preparation skills using various sources of read
	1	- Andrews	1110 2000	materials.
	[ ]		MC 156.4	Develop writing skills

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M	ESTER		MT 201.1	Distinguish between Ordinary and Partial Differential Equation & solve Linear, Non-Linear, Exact and Non-Exact Differential Equation of First Order.
			MT 201.2	Solve basic Application problems on Orthogonal Trajectory, L-R,L-C-R Circuits, Newtons law of Cooling, Growth and Decay explain by first order Differential Equations
	BS 201 MT	Mathematics -II	MT 201.3	Recognize a second & higher order differential equation and solve Homogenous and Non Homogenous second and higher order equation with constant and variable coefficient.
1	B3 201 MI	Matiematics =11	MT 201.4	Find the Laplace Transform of the Elementary functions and their derivatives to solve differential equation for initial value problems, periodic functions and use to find Inverse Laplace transform.
			MT 201.5	Solve nonsingular differential equation by the power series method and identify the singular points and ordinary points.
			MT 201.6	To demonstrate the ability to use Legendre's, Bessel's and Use of Beta- Gamma Functions to evaluate fractional calculus in approximating D.E.
1			PH 202.1	To apply the principles of Optics to calculate the wavelength of monochromatic and polychromatic light.
			PH 202.2	Using basic principles of statistical mechanics students can able to
		Party and an Alexandra III	PH 202.3	understand the materials and their properties.  Ability to understand the basic characteristics and design the
2	BS 202 PH	Engineering Physics - II	PH 202.4	semiconductor devices by using band theory of solids.  Ability to understand the arrangement of atoms and predict the defects
			PH 202.5	the crystals.  Ability to identify, distinguish and characterize the matrials and their properties related to Dielectric, Magnetic, Nano and Superconductors.
			CH 203.1	Apply the concept of electrode potential in identifying feasibility of electrochemical reaction; illustrate electro analytical techniques and
	85 203 CH		CH 203.2	working of batteries.  Identify the mechanism of corrosion of materials on basis of electrochemical approach and devise corrosion control methods.
3		Engineering Chemistry - II	CH 203.3	Estimate the physical & chemical parameters of quality of water and explain the process of water treatment.
3		Engineering Chemistry - II	CH 203.4	Explain the influence of chemical structure on properties of materials ar
			CH 203.5	their choice in engineering applications  Relate the concept of green chemistry for design of and manufacturing
			CH 203.6	engineering materials.  Classify chemical fuels and grade them through qualitative analysis.
			HS 253.1	recognize the importance of business communication and its different aspects
			HS 253.2	demonstrate awareness of different models of interpersonal
4	HS 204 EG	Business Comunication and Presentation Skills	HS 253.3	communication and use the skills for personality development apply appropriate writing styles for a variety of academic and profession
1			HS 253.4	requirements  compose career oriented written communication
			HS 253,5	demonstrate group discussion and interview skills
			CE 205.1	Determine the Center of gravity and Mass moment of inertia of solid an composite bodies. Extracting information regarding hidden variables or unknown in a system using Principle of Virtual work.
5	ES 205 CE	Engineering Mechanics -II	CE 205.2	Apply the laws of motion to study the kinematic parameters of rigid boo motion and its analysis in a plane. (Apply the knowledge of engineering fundamentals to the solution of complex engineering problems.
			CE 205.3	Solve the problems involving translation and rotation of particle & rigid bodies by applying principles of kinetics.
			CE 205.4	Apply the Principles of work- energy, its applications to bodies in Translation and in connected systems.
			CE 205.5	Analyze and solve impact problems using principles of impulse momentum.
			PH 251.1	Conduct experiments, take measurements and analyze the data through hands-on experience in order to demonstrate understanding of the theoretical concepts of applied physics, while working in small groups.
			PH 251.2	Demonstrate writing skills through clear laboratory reports.
6	BS 251 PH	Engineering Physics	PH 251.3	Interpret the principles of optics and determine the properties of mater graphically
		Laboratory -II	PH 251.4	Compare the experimental results with those introduced in fecture, dra relevant conclusions and substantiate them satisfactorily.
			PH 251,5	Transfer group experience to individual performance of experiments an demonstrate effective oral communication skills.

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			CH 252.1	Apply the concept of electrode potential in identifying feasibility of electrochemical reaction;
			CH 252.2	Illustrate electro analytical techniques and working of batteries
			CH 252.3	Identify the mechanism of corrosion of materials on basis of electrochemical approach and devise corrosion control methods.
7	BS 252 CH	Engineering Chemistry Laboratory-II	CH 252.4	Estimate the physical & chemical parameters of quality of water and explain the process of water treatment.
0			CH 252.5	Explain the influence of chemical structure on properties of materials and
- }			CH 252.6	their choice in engineering applications
			CH 252.7	Classify chemical fuels and grade them through qualitative analysis
			ME 155.1	Use proper tools and prepare the work piece in wooden material.
			ME 155.2	Use proper tools for marking, measuring, cutting and finishing the work piece on metal plate.
			ME 155.3	Prepare various types of joints by Arc welding process
8	ES 155 ME	Engineering Workshop -I	ME 155.4	Use suitable tools to make plumbing joint assembly for water supply.
			ME 155.5	With the Annual Control and the State Control and the Control
			ME 155.6	Prepare layout of electrical circuits and testing the circuits.
			CS 930.1	Apply the design concepts for development of process and interpret data.
9	ES 930 CS	Computer Skill Lab	CS 930.2	Demonstrate knowledge of programming environment, compiling, debugging, linking and executing variety of programs
			CS 930.3	Demonstrate documentation and presentation of the algorithms / flowcharts / programs in a record form
			CS 930.4	Validate the process using known input-output parameters
_			EG 253.1	Acquire listening competence.
10	HS 253 EG	Communication Skill Lab	EG 253.2	Enhance speaking skills with appropriate body language, through interactive activities.
	100 mag (12 m)		EG 253.3	Improve reading skills using various sources of reading materials.
			EG 253.4	Develop writing skills.
			CE 254.1	Draw the true shape of solids, sections of solids & 3D models using AutoCAD.
			CE 254.2	Demonstrate & draw the Development of surfaces.
11	ES 254 CE	Engineering Graphics Lab -II	CE 254.3	Draw the Intersection of surfaces.
			CE 254.4	Draw the isometric projections of the simple & combined solids.
			CE 254.5	Draw the Perspective views of straight lines, plane figures and simple solids.
			CE 255.1	Aquire basic concepts of Building Drawing
10	PC 255 CE	Building Drawing	CE 255.2	Aquire knowledge about application of different sign in building drawing
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CO-PO MAPPING OF I & II SEMESTERS CIVIL ENGINEERING DEPARTMENT

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1604-17-732-001	A	A	Α	С	D	С	S	S	S	Α	S	S	D	В	D	В	A	S		- :			
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1604-17-732-016	E	В	D	E	E,	E	S	В	D	Α	Α	S	E	E	E	D	Ô	A	A	S	S	S	S
1604-17-732-016	В	S	Α	С	E	С	s	Α	S	Α	Α	S	С	В	В	c	c	s	S	A S	A S	D	С
1604-17-732-017	B	S	A	В	D	С	S	S	Α	S	S	S	D	Α	C	В	A	S	5	5	5	Α	В
1604-17-732-019		D	E	E	E	С	S	В	С	С	Α	Α.	É	D	E	В	E	A	A	B	- 3	S	A
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1604-17-732-021	В		E	E	С	D	A	В	D	С	С	Α	E	E	E	E	D	В	A	В	Α	С	B
1604-17-732-022	A	C	Α .	A	E	D	S	Α.	В	5	Α	S	С	В	С	D	C	S	5	В	S	-	В
1604-17-732-025	D	D	Α	C	E	СС	S	S	Α	S	S:	Α	E	В	E	c	В	S	S	A	A .	- A	
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1604-17-732-033	C	E	c	D	D	C	S A	В	В	A	A	Α	D	C	E	В	D	Α	Α	Α	Α	Α	В
1604-17-732-034	D	D	D	E	F	c	A	В	В	5	S	S	С	С	D	В	D	Α	S	Α	S	Α	В
1604-17-732-035	D	D	c	E	c	D	A	B	В	A	A	A	E	E	E	C	D	Α	Α	В	Α	Α	D
1604-17-732-036	D	E	D	E	F	D	A	A	В	A	Α	В	E	С	D	В	D	Α	В	В	S	В	D
1604-17-732-037	D	E	E	F	-	В	В	В	B	A	A	Α	E	E	E	E	В	D	Α	Α	A	Α	A
1604-17-732-038	D	D	D	F	F	c	A	A	В	В	В	В	E	E	D	С	Ε	Α	С	В	В	С	D
1604-17-732-043	E	E	E	F	D	D	A	A	В	C	A	A	E	D	E	В	D	В	A	В	S	Α	С
1604-17-732-044	D	С	В	D	F	D	S	A	В	С	A	A	E	D	E	С	E	S	В	Α	Α	A	С
1604-17-732-046	D	E	D	E	D	D	A	В	В	A	5	Α	E	D	D	В	E	В	В	Α	Α	С	С
1604-17-732-047	Α	A	c	C	E	c	S	A	A	S	С	Α	E	E	D	В	E	Α	Α	В	A	С	D
1604-17-732-048	D	E	E	F	E	D	S	A	В	S	Α	A	C	В	С	В	D	Α	_ A	Α	Α	Α	С
1604-17-732-049	E	E	D	E	E	E	A	A	В	S	C	Α.	E	С	D	В	E	Α	A	_ A	S	В	A
1604-17-732-050	E	E	E	E	E	D	A	A	A	S .	A	A	E	D	E	С	D	В	A	A	S	С	С
1604-17-732-051	D	Ε	C	D	F	D	A	c	C	C	В	Α.	D	D	E	В	E	A.	С	В	S	Α	С
1604-17-732-055	Α	В	С	c	E	D	s	A	В		С	A	D	_ C	D	В	D	В	Α	C	Α	D	В
1604-17-732-057	E	E	E	F	E	E	A	В	C	A C	S	Α	E	_ с	D	Α	В	S	Α	C	Α	С	D
1604-17-732-058	D	E	С	E	E	D	A	B	c	C	В	A	E	E	E	С	E	В	A	С	Α	C	В
1604-17-732-061	Α	Α	A	c	D	c	s	S	В		В	A	С	_ с	С	В	C	В	В	В	S	В	В
1604-17-732-062	D	E	D	D	D	c	A	A	В	S	S	S	В	В	В	В	S	S	S	Α	S	S	Α
1604-17-732-063	В	В	В	D	D	c	5	S	S	_ A	S	A	E	С	E	D	E	В	A	В	Α	В	В
1604-17-732-064	Α	8	A	c	D	c	S	S	A	S	Α.	5	D	Α	D	С	В	A	A	В	S	S	С
1604-17-732-065	В	С	В	č	D	D	S .	A	A	A	_ A	A .	С	A	D	С	A	5	S	В	S	S	S
1604-17-732-066	С	С	С	D	D	c	A	В	A	A	A	A	Ε.	D	E	D	Ε	Α	Α -	В	S .	Α	Α
1604-17-732-067	В	С	c	D	E	D	S	A	C	B	Α .	A	D	D	E	С	D	В	Α .	В	Α	В	В
1604-17-732-068	С	С	c	c	D	D	S	A	S	A S	B	Α	С	С	С	D	С	Α	С	В	В	C	D
1604-17-732-071	Α	В	5	В	D	C	S	A	A A	5	3	5	D	В	С .	В	С	Α	S	В	\$	S	Α
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1604-17-732-087	В	E	D	С	E	D	A	A	B	A	Δ	Δ	E	-		<u> </u>			A	C	S		D
1604-17-732-088	D	С	D	D	E	D	A	A	В	A	Δ	Δ	F	В	B	c		e e	A	C	5	Α	
1604-17-732-090	Е	D	D	D	E	D	В	S	A	Α.	Α	Δ	D	F	-	É	-	B	Ā	В	S	^	A B
1604-17-732-091	Α	Α	В	В	Е	С	S	S	S	S	S	Ä	D	В	C	c	В	A	S	В	S	S	S
1604-17-732-092	С	С	D	D	E	D	В	Ā	Ā	A	A	A	E	В	D	D	D	A	A	C	S	Δ	c
1604-17-732-093	E	В	D	E	Е	D	S	Α	A	A	S	S	D	C	C	c	E	S	s	C	S	S	A
1604-17-732-094	В	Α	В	D	E	С	S	A	A	S	A	S	F	A	В	В	Ā	A	S	В	S	В	Â
1604-17-732-097	D	D	E	Е	Е	D	Α	Α	Α	С	S	A	E	D:	D	Ē	a	В	A	C	A	Δ	A
1604-17-732-103	Α	Α	В	В	В	D	S	Α	S	S	S	A	A	s	A	C	C	S	Â	D	S	s	ŝ
1604-17-732-104	E	E	E	E	D	Е	Α	Α	D	С	S	A	E	E	D	Ē	E	Ā	A	В	A	c	Ă
1604-17-732-105	E	Е	E	E	Е	E	Α	В	D	A	В	A	D	E	D	C	D D	S	В	В	S	C	ĉ
1604-17-732-106	D	С	С	D	Е	D	S	Α	Α	В	S	Α	E	С	E	C	E	В	S	В	Ā	Δ	Ā
1604-17-732-109	D	С	D	С	E	С	S	В	С	С	Α	A	E	C	D	C	E	A	В	В	S	В	В
1604-17-732-110	E	D.	D	E	Е	Ε	S	В	С	С	Α	В	E	Ē	E	D	E	В В	В	В	В	В	Ā
1604-17-732-112	D	D	D	E	E	D	S	В	В	С	A	A	Ē	D	E	c	E	A	S	В	S	В	s
1604-17-732-113	В	С	С	Ē	E	D	S	S	B	В	S	Α	E	В	D	D	D	Ā	Ā	В	S	S	Ā
1604-17-732-115	С	E	D	D	Е	D	В	Α	С	Α	S	A	Ē	D	D	D	D	В	В	В	A	C	Ĉ
1604-17-732-116	С	C	D	D	D	D	S	В.	D	С	Α	A	E	D.	E	С	E	В	Ā	C	A	c	Ā
1604-17-732-117	E	D	D	E	C	E	С	В	С	В	В	Α	E	E	Ē	Ē	E	В	В	В	A	Č	ĉ

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Muffakham Jah College Of Engineering & Technology, Banjara Hills, Road No.3, HYDERABAD - 500 034. A.P. COURSE OUTCOMES AND PROGRAM OUTCOMES

Establish the correlation between the courses and program outcomes (POs) and

Program Specific Outcomes (PSOs)

The Program outcomes and Program Specific outcomes are listed here for ready reference.

PO1: Engineering knowledge: Apply the knowledge of mathematics, science, engineering

fundamentals, and an engineering specialization to the solution of complex engineering

problems.

PO2: Problem analysis: Identify, formulate, research literature, and analyse complex

engineering problems reaching substantiated conclusions using first principles of

mathematics, natural sciences, and engineering sciences

PO3: Design/development of solutions: Design solutions for complex engineering problems

and design system components or processes that meet the specified needs with appropriate

consideration for the public health and safety, and the cultural, societal, and environmental

considerations.

PO4: Conduct investigations of complex problems: Use research-based knowledge and

research methods including design of experiments, analysis and interpretation of data, and

synthesis of the information to provide valid conclusions.

PO5: Modern tool usage: Create, select, and apply appropriate techniques, resources, and

modern engineering and IT tools including prediction and modelling to complex engineering

activities with an understanding of the limitations.

PO6: The engineer and society: Apply reasoning informed by the contextual knowledge to

assess societal, health, safety, legal and cultural issues and the consequent responsibilities

relevant to the professional engineering practice.

PO7: Environment and sustainability: Understand the impact of the professional

engineering solutions in societal and environmental contexts, and demonstrate the knowledge

of, and need for sustainable development.

PO8: Ethics: Apply ethical principles and commit to professional ethics and responsibilities

and norms of the engineering practice.

Muffakham Jah College Of Engineering & Technology, Banjara Hills, Road No.3, HYDERABAD - 500 034, A.P. PO9: Individual and team work: Function effectively as an individual, and as a member or

leader in diverse teams, and in multidisciplinary settings.

PO10: Communication: Communicate effectively on complex engineering activities with

the engineering community and with society at large, such as, being able to comprehend and

write effective reports and design documentation, make effective presentations, and give and

receive clear instructions.

PO11: Project management and finance: Demonstrate knowledge and understanding of the

engineering and management principles and apply these to one's own work, as a member and

leader in a team, to manage projects and in multidisciplinary environments.

PO 12: Life-long learning: Recognize the need for, and have the preparation and ability to

engage in independent and life-long learning in the broadest context of technological change.

**Program Specific Outcomes (PSOs)** 

PSO1: VLSI Design: The ECE Graduates will acquire state of art analysis and design skills

in the area of digital and analog VLSI design using modern CAD tools.

PSO2: Embedded System Design: The ECE Graduates will develop preliminary skills and

capabilities necessary for embedded system design and demonstrate understanding of its

societal impact.

PSO3: Communication and Signal Processing: The ECE Graduates will obtain the

knowledge of the working principles of modern communication systems and be able to

develop simulation models of components of a communication system.

PSO4: Software Design Engineering: The ECE Graduates will develop soft skills, aptitude

and programming skills to be employable in IT sector.

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Course Outcomes (COs) (SAR should include one course from each semester of study, however, COs should be prepared for all courses and made available as evidence, if asked)

#### Course outcomes of one course from each semester

	Course Name - PC221EC - Electronic Devices
D.CO. L.D.C.	Year of Study – 2020 – 21 Second Year Semester III
PC221EC	Obtain the characteristics of diode in forward and reverse bias and perform
.1	mathematical modeling of diode as a resistor and capacitor.
PC221EC	Perform analysis and design of a complete AC to DC converter(Eg: Mobile
.2	Charger) consisting of Rectifiers, Filters and regulators
PC221EC	Describe the construction and working of a Bipolar Junction Transistor in
.3	various modes (CE, CB, CC) and design circuits for stabilization and
D.COOLEG	compensation of a BJT.
PC221EC	Convert the BJT into its equivalent h parameter model and perform exact and
.4	approximate analysis of BJT Amplifiers in low, mid and high frequencies for
	all modes of operation.
PC221EC	Describe the construction and working of various JFET and MOSFET and
.5	design FET based amplifiers
	Course Name - PC231EC- Analog Electronic Circuits
	Year of Study 2020 21 Second Year Semester IV
PC231EC.1	Identify the components that effect the frequency response and analyze the
	single and multi stage amplifiers
PC231EC.2	Recognize the type of feedback and analyze its effect on amplifier
	characteristics
PC231EC.3	Calculate the frequency of oscillation for different types of oscillator circuits
	suited for various applications using Barkhausen's criterion
PC231EC.4	Identify the importance of power amplifiers and calculate the efficiencies of
	class -A, B, AB and examine the effect on distortion
PC231EC.5	Describe the working of tuned amplifiers and distinguish various types and
	analyze the effect of quality factor on its performance.

	Course Name - PC505EC- Microprocessors and Microcontrollers
	Year of Study - 2020 21 Third Year Semester V
PC505EC.	Describe the fundamental concept of an advanced microprocessor 8086 and its
1	architecture.
PC505EC.	Demonstrate program proficiency by writing efficient programs in assembly
2	level language using various addressing modes and instruction set of 8086.
PC505EC.	Identify functions of various programmable peripheral IC's in a
3	microprocessors based system and make aware of the different techniques of
	interfacing between the processor and peripheral devices.
PC505EC.	Distinguish between a microprocessor and microcontroller and describe the
4	architecture and instruction and programming of 8051 microcontroller.
PC505EC.	Apply the design concepts to interface a microcontroller based system to the
5	real world.
	Course Name - PC502EC - Digital Signal Processing
	Year of Study – 2020 21 Third Year Semester VI
PC502EC	Compute the Discrete Fourier transform (DFT), and develop the DIT-FFT
.1	DIF-FFT algorithm and evaluate their computational efficiency.
PC502EC	Design FIR filter using different windowing techniques and explain finite word
.2	length effects.
PC502EC	Design Butterworth and Chebyshev filters using Impulse Invariant Technique
.3	(IIT) and Bilinear transformation techniques, compare FIR and IIR filters
PC502EC	Formulate multistage implementation of sampling rate conversion and illustrate
.4	the process of interpolation and decimation
PC502EC	Explain the architectures of DSP processor TMS320C54XX and Analyze their
.5	instruction sets and addressing modes.

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	Course Name - PC702EC - VLSI Design
	Year of Study - 2020 21 4th Year Semester VII
PC702EC.	Apply the knowledge of Verilog HDL data types, system tasks and compiler
1	directives to write gate level and data flow level modeling programs
PC702EC.	Write verilog programs for logic blocks using behavioural modeling and switch
2	level modeling by applying concepts of Tasks and Functions and also model a
	system using Mealy and Moore state machines.
PC702EC.	Describe Basic MOS Transistor action: and Basic electrical properties of MOS.
3	Infer MOS inverters with different loads, Basic Logic Gates with CMOS and
	Transmission gate logic circuits, BiCMOS inverter.
PC702EC.	Design basic CMOS logic gates at circuit level and obtain its stick and layout
4	diagram using concept of CMOS fabrication and design rules.
PC702EC.	Compare the architectural difference between Combinational and Sequential
.5	Logic.
	Course Name – PW961 EC – Project
	Year of Study – 2020 – 21 4th Year Semester VIII
PW961	Demonstrate an in-depth knowledge of one or more areas of Electronics and
EC.1	Communication Engineering and integration of knowledge gained through
	several courses, by developing veritable solution to a complex problem.
PW961	(Templomistral Cottenalarity Contributions) a formatted report with proper layout,
EC.2	grammar, spelling, cross-referencing of figures, tables and references to
	previous works. (Report Writing)
PW961	Develop and present project plan making use of management tools like PERT,
EC.3	CPM, and UML diagrams by dividing the project work into suitable packages
	and identify resources for completion of the packages. (Project Planning)
PW961	Present results clearly making use of appropriate latest IT tools in the form of
EC.4	graphs, tables, drawing or text, analyze the results and state appropriate
	conclusions. (Results)
PW961	Exhibit a sound knowledge of the problem, its solutions and results through
EC.5	detailed presentation of the material and oral responses to the questions based
	on the work. (University viva-voce Examination)

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# CO-PO matrices of courses selected in 3.1.1(Six matrices to be mentioned, one per semester from 3rd to 8th Semester)

#### CO-PO matrices of courses selected in

Courses					Pro	gram	Outco	omes				
	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO1 0	PO1 1	P O 12
PC221EC -	3	2	3	3	2		Œ	/ <del>=</del> /	-	-	<b>-</b> v	( <b>-</b> )
<b>Electronic Devices</b>												
Second Year												
Semester I												
PC221EC .1:	3	2	漢	1	2	F=	->	=:	-	-	-	=
Obtain the												
characteristics of												
diode in forward												
and reverse bias												
and perform												
mathematical												
modeling of diode												
as a resistor and												
capacitor.										1		
PC221EC .2:	-	2	3	3	-	-	-			: <u>=</u>	ì	
Perform analysis												
and design of a												
complete AC to DC												
converter (Eg:												
Mobile Charger)												
consisting of												
Rectifiers, Filters												
and regulators												
PC221EC .3:	1-		-	3	2	-	-	.=		-	-	1-
Describe the										1	1	
construction and working of a									1	2/11	gr	T
wastering see s										et	3	12

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						— г						
Bipolar Junction												
Transistor in												
various												1
modes(CE,CB,CC)												
and design circuits for stabilization and												
compensation of a												
BJT.												
PC221EC .4:	-	1	2	-		3.	/ <del>-</del>		<u>u</u> :	-		-
Convert the BJT												
into its equivalent h												
parameter model												
and perform exact												
and approximate												
analysis of BJT												
Amplifiers in low,												
mid and high												
frequencies for all												
modes of operation												
PC221EC .5:	3	2	-	3	-		-	-	ų.	<b>-</b>	Œ	*
Describe the												
construction and												
working of various												
JFET and MOSFET												
and design FET												
based amplifiers PC231EC -	3	3	3	3	) <u> </u>	ļ	-	<u> </u>		4	-	20
Analog Electronic	5											
Circuits Second												
Year Semester II												
PC231EC.1:Identif	3	3	3	( <b>-</b> )	-,	-		-	<del>-</del>		*	-
y the components	l .											
that effect the												
frequency response										,	1	
and analyze the										nh	las	w
una unaryzo mo									4	19	5-3	03
	•									PRI	VCIPA	L

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			1									
single and multi												
stage amplifiers				2								_
PC231EC.2:Recog	-	3	<-:	3	-	<b>.</b>	5	•	3			
nize the type of												
feedback and												
analyze its effect on												
amplifier's												
characteristics												
PC231EC.3:Calcul	3	-	3	3	<b></b>	-	-	i iii	i <del>.</del>	<b>≘</b> :	任	-
ate the frequency of												
oscillation for								-				
different types of												
oscillator circuits												
suited for various												
applications using												
Barkhausen's												
criterion												
PC231EC.4:Identif	-	3	2	-		, <del>-</del>	v <del>z</del>	-,	*	*	9	*
y the importance of												
power amplifiers												
and calculate the												
efficiencies of class												
-A, B, AB and												
examine the effect												
on distortion												
PC231EC.5:	_	2	-	3	-5	-:		-	-		€	
Describe the												
working of tuned												
amplifiers and												
distinguish various												
types, analyze the	1											- 1
effect of												
neutralization										/	7	n
nout unization									1	201	2	25
							L				23/6	7

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techniques to												
improve the												
stability.												
EC 305 -	3	2	2	2	2	:	. <del>*</del>	<u>~</u>	-		-	-
Microprocessors												
and												
Microcontrollers												
Third Year									1			
Semester I								1				
PC505EC.1:	1	2	<u>:</u>	2	:-:	-	-	-	-		÷	*
Describe the												
fundamental												
concept of an												
advanced												
microprocessor												
8086 and its												
architecture.												
PC505EC.2:	3	2	-	2	2	-	-		-	) <del>-</del>	œ	-
Demonstrate												4
program												
proficiency by												
writing efficient												
programs in												
assembly language												
using various												
addressing modes												
and instruction set												
of 8086.												
PC505EC.3:	3	-	2	2	2	-	-:	•	=		-	->
Identify functions												-
of various												
programmable											7	1 AR
peripheral ICs in a										20	for	- In

		т т										
microprocessor												
based system and												
make aware of the												
different techniques												
of interfacing												
between the												
processor and												
peripheral devices.												
PC505EC.4:	3	2	2	2	2	•	=	-	-			
Distinguish												
between a												
microprocessor and												
microcontroller and												
describe the												
architecture												
,instruction and												
programming of												
8051												
microcontroller.												
PC505EC.5:Apply	3	2	3	2	2	<del>-</del> 2	-	-	¥	ž	<b>E</b>	<u>.</u>
the design concepts												
to interface a												
microcontroller												
based system to the												
real world,												
EC 352 - Digital	3	2	3	2	-	-	-		-	-	-	-
Signal Processing												
Third Year												
Semester II												
PC502EC .1:	-3	2	-	2	-	-	-	-	·=	=:	\ <del>-</del>	(F)
Compute the												
Discrete Fourier										6	an	w
transform (DFT),									1	20/	27	3/
										RINCI	1	-

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<del></del>												1
and develop the												
DIT-FFT, DIF-FFT												
algorithm and												
evaluate their												
computational												
efficiency.												
PC502EC	<u>=</u> v	3	3	-	*	:-	-	. <del>-</del>	.=.	₹:	=:	<b>(</b>
.2:Design FIR filter												
using different												
windowing												
techniques and												
explain finite word												
length effects.												
PC502EC	-	3	3	1		=:	<b>-</b> 5	*	=	-	۰	<b>-</b> s
.3:Design												
Butterworth and												
Chebyshev filters												
using Impulse												
Invariant												
Technique (IIT)												
and Bilinear												
transformation												
techniques,												
compare FIR and												
IIR filters												
PC502EC	3	3		*	-	-	-	-	\.	->	10-	-
.4:Formulate												
multistage												
implementation of												
sampling rate												
conversion and												
illustrate the												
process of										1	gu	en
process or									N	20/0	23	00
	J:	L				1			P	RINCI	PAL	

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interpolation and												
decimation.												
PC502EC	:=	2	2	1	×	4	*	æ	14	-	-	-
.5:Explain the												
architectures of												
DSP processor												
TMS320C54XX												
and Analyze their												
instruction sets and												
addressing modes.												
EC 402 – VLSI	3	2	2	2	~	-	2) <del>-</del>		-	-	-	7/
Design												
Final Year											-	
Semester I												
PC702EC.1: Apply	3	-	2	2	>=	<-	1-1	-	-	-	-	
the knowledge of												
Verilog HDL data												
types, system tasks												
and compiler												
directives to write												
gate level and data												
flow level												
modeling programs												
PC702EC.2: Write	3	2	2	2			-	-	-	-	-	-
verilog programs												
for logic blocks	1											
using behavioral										1		
modeling and												
switch level												
modeling by												
applying concepts												
of Tasks and												
Functions and also											Da	w
										n	1	2

					-							
model a system												
using Mealy and												
Moore state												
machines.		2										
PC702EC.3:	3	2	2	-	-	-	:=.	-	-	•	<u>™</u>	*
Describe Basic												
MOS Transistor									10			
action: and Basic												
electrical properties												
of MOS. Infer												1
MOS inverters with												
different loads,		l U										
Basic Logic Gates												
with CMOS and												
Transmission gate												
logic circuits,												
BiCMOS inverter.												
PC702EC.4:	2	3	2	( <b>.</b>	4 <del>-</del>			€	*	¥	-	27
Design basic												
CMOS logic gates												
at circuit level and												
obtain its stick and												
layout diagram												-
using concept of	1											
CMOS fabrication												
and design rules.												
PC702EC.5:	2	3	3	1			-	-		-	-	-
Compare the												
architectural												
difference between												
Combinational and												
Sequential Logic.										1		
EC 482 –	3	3	3	3	3	2	2	2	3	3	2	3
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ProjectFinal Year												
Semester II												
PW961 EC.1:	3	3	3	3	3	2	2	38)	2	Œ	2	3
Demonstrate an in-												
depth knowledge of												
one or more areas										v		
of Electronics and												
Communication												
Engineering and												
integration of												
knowledge gained												
through several												
courses, by												
developing												
veritable solution to												
a complex problem.												
(Technical Content												
and Contribution)												
PW961 EC.2:	-	-	-	-			) <del>-</del>	2	3	3	3	
Demonstrate the												
ability to produce a												
formatted report												
with proper layout,												
grammar, spelling,												
cross-referencing of												
figures, tables and												
references to												
previous works.												
(Report Writing)												
PW961 EC.3:		*		-	1-	-		-	-	2	3	2
Develop and												
present project plan												
making use of										-1	200	m
									-	100	105	10

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												-
management tools					Į.				1			
like PERT, CPM,												
and UML diagrams												
by dividing the												
project work into												
suitable packages												
and identify												
resources for												
completion of the									1			
packages. (Project												
Planning)												
PW961 EC.4:	2	3	3	3	3	<b>=</b> :	<b>L</b> ,	-	3	3	-	2
Present results												
clearly making use												
of appropriate latest												
IT tools in the form												
of graphs, tables,												
drawing or text,												
analyze the results												
and state												
appropriate												
conclusions.												
(Results)												
PW961 EC.5:	3	2	3	2	2	2	2	2	3	3	2	2
Exhibit a sound	J	_						-	-			
knowledge of the problem, its												
solutions and												
results through												
detailed presentation of the								ľ				
material and oral												
responses to the questions based on												
the work.							1					
(University viva- voce Examination)								l,				
, occ Establishment									1	nhi	au	w
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Note: 1: Slight Correlation 2: Moderate Correlation 3: High Correlation -: NO Correlation

B) CO-PSO matrices of courses selected in 3.1.1(Six matrices to be mentioned, one per semester from 3<sup>rd</sup> to 8<sup>th</sup> Semester)

### CO-PSO matrices of courses selected in Table

	P	rogram	Specifi	c
Castinaran	(	Outcome	es(PSOs	)
Courses	PSO	PSO	PSO	PSO
	1	2	3	4
EC 203 – Electronic Devices Second Year Semester I	3	1		-
PC221EC .1: Obtain the characteristics of diode in forward	2	-	*	-
and reverse bias and perform mathematical modeling of				
diode as a resistor and capacitor.				
PC221EC .2: Perform analysis and design of a complete AC	2	1	-	-
to DC converter (Eg: Mobile Charger) consisting of				
Rectifiers, Filters and regulators				
PC221EC .3: Describe the construction and working of a	3	1=	-	(-
Bipolar Junction Transistor in various modes (CE, CB, CC)				
and design circuits for stabilization and compensation of a				
BJT.				
PC221EC .4: Convert the BJT into its equivalent h	3	an.	I	•>
parameter model and perform exact and approximate				
analysis of BJT Amplifiers in low, mid and high frequencies				
for all modes of operation				
PC221EC .5: Describe the construction and working of	3	-	-	<b>=</b> :
various JFET and MOSFET and design FET based				
amplifiers				
PC231EC - Analog Electronic Circuits Second Year	3	1	2	-
Semester II		1	1 h	our
PC231EC.1:Identify the components that effect the	3	10	16	230
	1		PRINC	IPAL

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	T	— т		
frequency response and analyze the single and multi stage				
amplifiers				
PC231EC.2:Recognize the type of feedback and analyze its	3	1	¥	÷.
effect on amplifier's characteristics				
PC231EC.3:Calculate the frequency of oscillation for	3	1	2	*:
different types of oscillator circuits suited for various				
applications using Barkhausen's criterion				
PC231EC.4:Identify the importance of power amplifiers and	2	-	7-	
calculate the efficiencies of class -A, B, AB and examine the				
effect on distortion				
PC231EC.5: Describe the working of tuned amplifiers and	1			<b>.</b>
distinguish various types, analyze the effect of neutralization				
techniques to improve the stability.				
PC 505 – Microprocessors and Microcontrollers Third	1	3		2
Year Semester I				
PC505EC.1: Describe the fundamental concept of an	1	3	•.	
advanced microprocessor 8086 and its architecture.				
PC505EC.2: Demonstrate program proficiency by writing	-	3	-	2
efficient programs in assembly language using various				
addressing modes and instruction set of 8086.				
PC505EC.3: Identify functions of various programmable	1	3	-	-
peripheral ICs in a microprocessor based system and make				
aware of the different techniques of interfacing between the				
processor and peripheral devices.				
PC505EC.4: Distinguish between a microprocessor and	1	3	<b>3</b>	2
microcontroller and describe the architecture, instruction and				
programming of 8051 microcontroller.				
PC505EC.5:Apply the design concepts to interface a	*	3	-	-
microcontroller based system to the real world,				
EC 352 – Digital Signal Processing Third Year Semester		-	3	-
П				
		-	3	103
PC502EC .1: Compute the Discrete Fourier transform				11/0
PC502EC .1: Compute the Discrete Fourier transform  (DFT), and develop the DIT-FFT, DIF-FFT algorithm and	_	W	100	1

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evaluate their computational efficiency.			2	
PC502EC .2: Design FIR filter using different windowing	-	-	3	-
techniques and explain finite word length effects.				
PC502EC .3:Design Butterworth and Chebyshev filters	.=:	=	3	-
using Impulse Invariant Technique (IIT) and Bilinear				
transformation techniques, compare FIR and IIR filters				
PC502EC .4: Formulate multistage implementation of	-	: <b>-</b>	2	-
sampling rate conversion and illustrate the process of				
interpolation and decimation.				
PC502EC .5: Explain the architectures of DSP processor	-	2	3	<b>=</b> ½
TMS320C54XX and Analyze their instruction sets and				
addressing modes.				
PC 702 – VLSI Design Final Year Semester VII	3	*	\ <u>-</u>	2
PC702EC.1: Apply the knowledge of Verilog HDL data	3	-	-	2
types, system tasks and compiler directives to write gate				
level and data flow level modeling programs				
PC702EC.2: Write verilog programs for logic blocks using	3	-	€/.	2
behavioural modeling and switch level modeling by applying				
concepts of Tasks and Functions and also model a system				
using Mealy and Moore state machines.				
PC702EC.3: Describe Basic MOS Transistor action: and	3		¥	i <del>e</del>
Basic electrical properties of MOS. Infer MOS inverters with				
different loads, Basic Logic Gates with CMOS and				
Transmission gate logic circuits, BiCMOS inverter.				
PC702EC.4: Design basic CMOS logic gates at circuit level	3		*	=
and obtain its stick and layout diagram using concept of				
CMOS fabrication and design rules.				
PC702EC.5: Compare the architectural difference between	2	-	je i	
Combinational and Sequential Logic.				
PW961EC - Project Final Year Semester II	3	3	3	3
PW961 EC.1: Demonstrate an in-depth knowledge of one or	3	3	3	2,
more areas of Electronics and Communication Engineering				
and integration of knowledge gained through several		1	261	an
		( )	-6	30

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courses, by developing veritable solution to a complex				
problem. (Technical Content and Contribution)				
PW961 EC.2: Demonstrate the ability to produce a formatted	3	3	3	3
report with proper layout, grammar, spelling, cross-				
referencing of figures, tables and references to previous				
works. (Report Writing)				
PW961 EC.3: Develop and present project plan making use	2	2	2	2
of management tools like PERT, CPM, and UML diagrams				
by dividing the project work into suitable packages and				
identify resources for completion of the packages. (Project				
Planning)				
PW961 EC.4: Present results clearly making use of	3	3	3	3
appropriate latest IT tools in the form of graphs, tables,				
drawing or text, analyze the results and state appropriate				
conclusions. (Results)				
CO482.5: Exhibit a sound knowledge of the problem, its	3	3	3	3
solutions and results through detailed presentation of the				
material and oral responses to the questions based on the				
work. (University viva-voce Examination)				

Note: 1: Slight Correlation 2: Moderate Correlation 3: High Correlation -: NO Correlation

# a) Program level course - PO matrix of all courses including first year courses(10)

#### Table Course-PO matrix for all courses

						Pro	gran	ı Ou	tcon	ıes			
Course Code	Course Title	P O 1	P O 2	P O 3	P O 4	P O 5	P O 6	P O 7	P O 8	P O 9	P O 10	P O1 1	PO 12
		<u> </u>	Firs	t Ye	ar (2	020	21	)					
HS101EG	English				-		75-	-	-	-	3	-	2
BS102MT	Mathematics 1	3	3	<b>*</b> .	-	-1			-		*		2
BS103MT	Mathematics 2	3	3	#5	-		-	-	1-1	-	-	-	3
BS104PH	Physics	3	3		*			-	-	1_1	-/	7-	3
BS105CH	Chemistry	-	3	2	2	-	3	3	-/	3	20/	3º	TO 2

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ES107CS	Programs for problem solving	2	2	2	1	)=	-		-	=	₹/	1	/ <u>#</u>
BS152PH	Physics Lab	3	3	-	3	<b>3</b> /	-	-	-	-	_	-	-
BS153CH	Chemistry Lab	3	3	*	3	:	-	-	×	¥	-	•	<u>-</u> v
ES157ME	Workshop	2	-		-		1	3	)-	-	-	-	-
ES155CS	Programs for problem solving lab	. <del></del>	8	=	3	2	-	-:	-		-	-	-
HS151EG	English Lab	( <b>a</b>	-	-	-	-	-	-,	-	3	3	1	2
	Second Y	ear	First	seme	ester	(202	0 2	21)					
BS205MT	Mathematics – III (PDE, Probability & Statistics)	3	2	-	=	=	=	-	_	/=	7-	<u>:</u>	-
PC221EC	Electronic Devices	3	2	3	3	2	-	-	-		- 1		
ES212ME	Elements of Mechanical Engineering	3	3	3	,-	-	-	-	•	*	-	=-	-
PC251EC	Electronic Devices lab	÷	-	-	3	2	-	-	-	2	3	<b>.</b>	1 507
MC111PO	Indian Constitution												
HS201EG	Effective Technical Communication in English												
HS202CM	Finance and Accounting												
ES216EC	Digital Electronics												
PC222EC	Network Theory												
PC252EC	Electronic Workshop	2	-	-	3	2	9	-	142	2	3	i-	*
	Second Ye	ar S	econ	d Sei	meste	er ( <b>2</b> 0	020 -	- 21)					
PC231EC	Analog Electronics Circuits	3	3	3	3	-	-			)=	·	)-	-
MC113PY	Essence of Indian Traditional Knowledge												
BS206BZ	Biology for Engineers												
HS213MP	Industrial Psychology										1		w
PC232EC	Electromagnetic Theory	3	3	3	2=	-	-	-	/	22	DE	200	13

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													$\neg \neg$
	and Transmission Lines												
ES215EC	Signals and Systems	3	3	; <b>-</b>	1	72	9	•	-		<b>∓</b> 1	-	*
PC233EC	Pulse and Linear Integrated Circuits	2	3	3	1	3 <del>4</del>	2	-	i.	*	-	*	: <del>-</del>
PC234EC	Computer Organisation and Architecture												
MC112CE	Environmental Sciences				=	=	3	3	3		7		<b>3</b> 5
PC261EC	Analog Electronic Circuits Lab	₹ <b>-</b>	-	2	3	2	-		,'E	2	3	-	
PC262EC	Pulse and Linear Integrated Circuits Lab	<b>=</b> X	1	-	3	-	-	-	-	2	3	5 <del>π</del>	€.
	Third Ye	ar F	irst S	eme	ster (	2020	2	1)		•			
PC502EC	Digital Signal Processing	3	2	3	1	æ	æ	=	2		-)	- 0	-
PC501EC	Analog Communications	3	2	1	1	:	-	-	-	-		*	=
PC503EC	Automatic Control Systems	3	2	2	<b>.</b>	3.5	33	=	-	-	-	-	) <b>-</b>
PC505EC	Microprocessors and Microcontrollers	3	2	2	2	2	<-	-	:-	æ	-	=	i e
EC 331	Integrated Circuits Lab		-	2	3	2	-	χĒ	<u> </u>	2	3	-	
PC551EC	Systems and Signal Processing Lab			2	3	2		( <b>-</b>	-	2	3	-	<b>.</b>
PC552EC	Microcontroller Lab	3	-	÷	3:	3	1	-/	-	2	3		-
	Third Yea	ar Se	conc	Sem	este	(20	20	21)					
PC601EC	Digital Communication	3	3	34	1	-	-	-	-	->	-	-	
PC602EC	Digital system Design with Verilog												
PC603EC	Data Communication and computer networking		1	-	3	) in the second	3 €		-	22	RINC	3	3/03

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PC604EC	Electronic												
PC604EC	VIII VIII SAN ANTONIO												
	W												
	Instrumentation												
OE - I	Open Elective-I												
PE – I	Professional Elective-I												
PC651EC	Digital	7 <del></del>	:	2	3	2	-, T	1 <del>-</del> 1	λĒ	2	3	ž	<b>3</b> 1
	Communication lab												
PC652EC	DCCN Lab	. <del>-</del>	.==.		3	3	36	#1	-	2	3	-	-
PC653EC	Digital system Design												
	with Verilog Lab												
PC654EC	Summer Internship*		-	-	-	-	-	-	-	2. <b>-</b> 1	-	i <del>,</del>	***
	Fourth Y	ear I	irst	seme	ster	(2020	0 2	1)					
PC 702 EC	VLSI Design	3	2	3	1.	-	-	-	-	=-/	( <b>-</b> )	( <b>-</b> )	-
PC 703 EC	Microwave Techniques												
PE 721	Mobile Cellular	3	-	2	\-		2	2	-		-	-	3
	Communication												
PC 701	: Embedded Systems	-	2	2	2	2	3	*	-	•.	-	2	2
ES 707 ME	Industrial	-		7=1	-	:-	3	2	2	2	3	3	, <b></b>
	Administration and												
	Financial Management												
OE 774EE	Non-Conventional												
	Energy Sources 5			h									
OE 775ME	Entrepreneurship												
OE 771 CE	Green Building		1	-									
	Technologies												
PE 723 EC	Electronic												
	Measurements and												
	Instrumentation												
OE 781 CE	Road Safety Engineering												
OE 782 IT	Software Engineering												
			_	1	1	1	1	_		-			
PC 751 EC	Microwave	-	*	2	3	2		Y=	-	2	3	-	2/20

					_	_	_						
PC 752 EC	Electronic Design and	3	, <u>-</u>	-	3	3	-	-	-	2	3	4.	•
	Automation Lab												
PW 761 EC	Project Seminar	3	3	3	3	3	2	2	2	3	3	2	3
SI 762 EC	Summer Internship												
	Fourth Year	ır Se	cond	Sem	iestei	r (20	20	21)					
PE 824 EC	Satellite	3	3	2		14	2	2	-	-	-		-
	Communication												П
PE 843 EC	Radar Systems												
PE 844 EC	Elective 2: Design of	=×	2	3	-	1:	~=	3	-	-	-	-	1,-
	Fault Tolerant Systems												
PE 841 EC	Elective 2: Real time	2	1	2	-	->	3	y=-	-		*	8	
	operating systems												
PE 832 EC	Elective 3: Global	3	3		-	-	3	3	-	-	-	-	-
	Navigational Satellite					l							
	Systems												
PE 823 EC	Elective 3: Neural	3	3	2	2	-			-	z. <del>-</del> -	-	-	₩.
	Networks												
PE 824 EC	Satellite Communications												
PE 834 EC	Multirate Signal												
	Processing												
PE 831 EC	Wireless Sensor												
	Networks												
PE 822 EC	Internet of Things												
PE 821 EC	Field Programmable Gate												
	Arrays												
PE 833 EC	System Verilog												
PW961 EC	Project	3	3	3	3	3	2	2	2	3	3	2	3

Note: 1: Slight Correlation 2: Moderate Correlation 3: High Correlation -: NO Correlation

# b) Program level course - PSO matrix of all courses including first year courses

Table Course-PSO matrix for all courses

Course Course Title Program Specific Outcomes (PSOS) 03 200

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Code		PSO1	PSO2	PSO3	PSO 4
	Fi	irst Year (202	20 - 21)		
HS101EG	English	-	2.■	-	3
BS102MT	Mathematics 1	2	-	2	=:
BS103MT	Mathematics 2	2	5=	2	<del></del>
BS104PH	Physics	A A	-	-	-
BS105CH	Chemistry	1.5	=:	*	-
ES107CS	Programs for problem solving	-	3	•	3
BS152PH	Physics Lab		-	y <u>-</u>	-
BS153CH	Chemistry Lab	.=	-	-	-
ES157ME	Workshop	2	-	-	-
ES155CS	Programs for problem solving lab	2			-
HS151EG	English Lab	<b>E</b>	-	=:	1-
HS101EG	English	-	3	-	3
BS102MT	Mathematics 1	=	€	-	2
	Second Y	ear first Sem	ester (2020 2	21)	
BS205MT	Mathematics – III (PDE, Probability & Statistics)	-	-	2	-
PC221EC	Electronic Devices	2	4.7	¥	
ES212ME	Elements of Mechanical Engineering	:-		2	-
PC251EC	Electronic Devices lab	3	-	-	
MC111PO	Indian Constitution		9	t.=	-
HS201EG	Effective Technical Communication in English	<b>=</b> 1	-		-
HS202CM	Finance and Accounting	3	-	-	
ES216EC	Digital Electronics	2	2	-	( <u>u</u>
PC222EC	Network Theory			1	M
PC252EC	Electronic Workshop		/	mpla	TOST

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	Second Year	r second Sem	ester (2020 )	21)	
PC231EC	Analog Electronics Circuits	=	->	2	
MC113PY	Essence of Indian Traditional Knowledge	3	-	2	8
BS206BZ	Biology for Engineers	7=	¥	2	-
HS213MP	Industrial Psychology		ě	3	-
PC232EC	Electromagnetic Theory and Transmission Lines	-	3	/=	
ES215EC	Signals and Systems	<b>=</b>	-	-	:=
PC233EC	Pulse and Linear Integrated Circuits	3	-	<b>=</b> :	æ
PC234EC	Computer Organisation and Architecture	-	-	<del>≡</del> n	æ
MC112CE	Environmental Sciences				
PC261EC	Analog Electronic Circuits Lab				
PC262EC	Pulse and Linear Integrated Circuits Lab				
	Third Yea	r First Semes	ster (2020 21	)	
PC502EC	Digital Signal Processing	2	2	-	1.9
PC501EC	Analog Communications	2	3	-	•
PC503EC	Automatic Control Systems	2	-	3	•
PC505EC	Microprocessors and Microcontrollers	2	2	2	Ŧ.
EC 331	Integrated Circuits Lab	:=	3	-	<b>.</b>
PC551EC	Systems and Signal Processing  Lab	3	2	-	ē
PC552EC	Microprocessor and Microcontroller Lab	). A. <del>=</del>	Ð	3	-
	Third Year	Second Sem	ester (2020	21)	
PC601EC	Digital Communication	1	=	3	-
PC602EC	Digital system Design with Verilog	-	-	3	<b>:</b> =
PC603EC	Data Communication and	=	9	Elan	w

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	computer networking				
PC604EC	Electronic Measurements and Instrumentation	-	2	-	-
OE – I	Open Elective-I	2	2	-	=,
PE-I	Professional Elective-I	1	1	1	₩/
PC651EC	Digital Communication lab	¥	7.=	3	**
PC652EC	DCCN Lab		1.7	3	2
PC653EC	Digital system Design with Verilog Lab	2	2	2	2
PC654EC	Summer Internship*				
	Final Yea	r First Semes	ster (2020 2)	()	
PC 702 EC	VLSI Design	: <del>=</del>	<del></del> /	2	*
PC 703 EC	Microwave Techniques	3	1	<b>⊹</b>	-
PE 721	Mobile Cellular			2	
	Communication	-	-	2	<u>-</u>
PC 701	Embedded Systems		Ψ.	3	-
ES 707 ME	Industrial Administration	_	3	_	_
	and Financial Management	=	3.		
OE 774EE	Non-Conventional Energy	-	_	3	
	Sources 5				
OE 775ME	Entrepreneurship	1	1	1	
OE 771 CE	Green Building Technologies	<u>.</u>	-	2	×
PE 723 EC	Electronic Measurements and	3		_	74
	Instrumentation				
OE 781 CE	Road Safety Engineering	2	2	2	2
OE 782 IT	Software Engineering				
PC 751 EC	Microwave Engineering Lab				
PC 752 EC	Electronic Design and				
	Automation Lab				
PW 761 EC	Project Seminar				
SI 762 EC	Summer Internship				
	Final Year	Second Sem	nester (2020	21)	w
PE 824 EC	Satellite Communication	<b>-</b>	- /	1/2/12	1

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PE 843 EC	Radar Systems	2	<u>-</u>	-	-
PE 844 EC	Elective 2: Design of Fault Tolerant Systems	=	3	-	<b>=</b> :
PE 841 EC	Elective 2: Real time operating systems	) <del>.</del>	₽	3	2
PE 832 EC	Elective 3: Global Navigational Satellite Systems	( <del>-</del>	<del>-</del>	2	
PE 823 EC	Elective 3: Neural Networks	2	2	2	2
PE 824 EC	Satellite Communications	3	3	3	3
PE 834 EC	Multirate Signal Processing				
PE 831 EC	Wireless Sensor Networks				
PE 822 EC	Internet of Things				
PE 821 EC	Field Programmable Gate Arrays				
PE 833 EC	System Verilog				
PW961 EC	Project				

### Mapping of Curriculum with POs and PSOs

Course Component	Curriculum Content( credits of prog		POs	PSO
	Mathematics-I		1,2 and 12	
Mathematics	Mathematics-II	6.48%	1,2 and 12	
	Mathematics-III		1 and 2	
Basic	Physics	5.55%	1,2 and 12	our

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	Chemistry		2,3,4,6 and 7	
	Physics Lab		1,2 and 4	
	Chemistry Lab		1,2 and 4	
	Programming in C & C++	2 770/	1,2,3,4 and 11	PSO 2, PSO 4
Computing	Programming Lab	2.77%	4	PSO2, PSO 4
	English		10 and 12	PSO4
Humanities	English Lab	4.62%	9,10,11 and 12	PSO4
	Environmental Sciences	1	6,7 and 8	
Professional	Basic Circuit Analysis	54.62%	1,2,4 and 5	PSO 1
Core	Electromagnetic Theory		1,2 and 3	
	Electronic Devices		1,2,3,4 and 5	PSO 1
	Electronic Devices Lab		4,5,9 and 10	
	Electronic & Workshop Lab	1	1,4,5,9 and 10	
	Analog Electronic Circuits	1	1,2,3 and 4	PSO 1
4	Network Transmission Lines		1,2 and 3	
	Signal Analysis and Transform Techniques		1,2 and 4	
	Pulse Digital And Switching Circuits		1,2,3 and 4	PSO 2
	Electronic Circuits Lab		2,4,5,9 and 10	
	Linear Integrated Circuits and Applications		1,2,3 and 4	PSO 1
	Digital Integrated Circuits and Applications		1,2,3 and 4	PSO 1
	Analog Communication		1,2,3 and 4	
	Microprocessors and Microcontrollers		1,2,3 ,4 and 5	PSO 2
	Integrated Circuits Lab		3,4,5,9 and 10	PSO 2
	Automatic Control Systems		1,2 and 3	
	Analog Communication Lab		3,4,5,9 and 10	
	Microprocessors and Microcontrollers Lab		1,4,5,6,9 and 10	PSO 2

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	Digital Communication		1,2 and 4	
	Digital Signal Processing		1,2,3 and 4	
	Antenna and Wave Propagation		1,2 and 3	
	Computer Organization and Architecture		1,2 and 3	PSO 2
	Electronic Instrumentation		1,2 and 3	
	Digital Communication lab		3,4,5,9 and 10	
	Digital Signal Processing Lab		4, 5, 9 and 10	PSO 3, 4
	Microwave Engineering		1,2,3 and 4	
	VLSI Design		1,2,3 and 4	PSO 1
	Computer Networking		1,2,6,8 and 12	PSO 3
	Mobile Cellular Communication		1,3,6,7and 12	
	Microwave Engineering Lab		3,4,5 and 10	
	Electronic Design and Automation Lab		1,4,5,9 and 10	PSO 1, 4
*,	Radar and Satellite Comm.		1,2,3,6 and 7	
Engineering	Engineering Graphics	14.11%	1,3 and 5	
Sciences	Workshop Practice Lab		1,6 and 7	
	Elements of Mechanical Engineering	J	1,2,3 and 4	ı
	Electrical Technology		2,4 and 6	
	Electrical Technology Lab		2,4,9 and 10	
	Managerial Economics and Accountancy		1,2,4 and 11	
V	Industrial Administration and Financial Management		6,7,8,9 and 11	
	Embedded Systems		2,3,4,5,6 and 12	PSO 2
	Digital Image Processing		1,2,3 and 11	
Electives	Design of Fault Tolerant Systems	5.55%	2,3 and 7	
	Real Time Operating System		1,2,3 and 6	
	Neural Network and Fuzzy Logic		1,2,3 and 4	
Seminars	Industrial Visit/Study	5.55%	1 to 12 16 2	PSO 1,2

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and project			3
	Project Seminar	1 to 12	PSO 1, 2
	Seminar	1 to 12	PSO 1, 2 3,4
	Project		

In order to identify curricular gaps a separate table is given below in which each program outcome and program specific outcome is explicitly mentioned along with the courses that strongly map to the POs and PSOs.

Table 1 Mapping of POs and PSOs to identify curricular gaps

Program Outcome	Courses catering to POs and PSOs
PO1: Engineering knowledge: Apply the	Mathematics I, II, III and IV
knowledge of mathematics, science, engineering	Engineering Physics
fundamentals, and an engineering specialization	Engineering Chemistry
to the solution of complex engineering problems.	Engineering Mechanics
	Basic Circuit Analysis
	Electromagnetic Theory
	Signal Analysis and Transform Techniques
	Pulse Digital and Switching circuits
PO2: Problem analysis: Identify, formulate,	Electronic Devices
research literature, and analyse complex	Analog Electronics Circuits
engineering problems reaching substantiated	Analog Communications
conclusions using first principles of mathematics,	Automatic Control Systems
natural sciences, and engineering sciences.	Digital Communication
matara social so	Digital Signal Processing
	Antenna and Wave Propagation
	Electronic Instrumentation
	Project Seminar
	Project
PO3: Design/development of solutions: Design	Linear Integrated Circuits and Applications
solutions for complex engineering problems and	Digital Integrated Circuits and Applications

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Understand the impact of the professional	Engineering Workshop  Environmental Studies
PO7: Environment and sustainability:	Chemistry  Engineering Workshop
	Project
	General Seminar
engineering practice.	Project Seminar
responsibilities relevant to the professional	Management
cultural issues and the consequent	Industrial Administration and Financial
to assess societal, health, safety, legal and	Environmental Studies,
reasoning informed by the contextual knowledge	Electrical Technology,
PO6: The engineer and society: Apply	Chemistry,
activities with an understanding of the limitations.	Digital Signal Processing Lab(MATLAB Software)  Electronic Design and Automation Lab(Softwares used - Mentor, Xilinx, Tanner, Cadence)
modern engineering and IT tools including prediction and modelling to complex engineering	Microprocessors and Microcontrollers Lab(KEIL Software)
PO5: Modern tool usage: Create, select, and apply appropriate techniques, resources, and	Electronic Workshop and Basic Circuits Lab(SPICE Tools)
conclusions.	Microprocessors and Microcontrollers Lab Microwave Engineering Lab Electronic Design and Automation Lab
and synthesis of the information to provide valid	Communication Lab
research methods including design of experiments, analysis and interpretation of data,	Electronic Circuits Lab Integrated Circuits Lab
problems: Use research-based knowledge and	Electronic Workshop and Basic Circuits Lab
PO4: Conduct investigations of complex	Electronic Devices Lab
environmentalconsiderations.	Design of Fault Tolerant Systems Real Time Operating System
and the cultural, societal, and	Embedded Systems Digital Image processing
consideration for the public health and safety,	VLSI Design
the specified needs with appropriate	Computer Organization and Architecture

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Design of Fault Tolerant Systems	
General Seminar	
Computer Networking	
Managerial Economics and Industrial	
Administration	
Project	
English	
Project Seminar	
Project	
And Laboratories	
English	
English Lab	
All laboratory courses of department	
Project Seminar	
General Seminar	
, General Seminar Project	
Managerial Economics and Accountancy	
Industrial Administration and Financial	
Management	
Project Seminar	
Project	
Project Seminar	
Project	
R&D Projects	
Sponsored projects by SUES	
ific Outcomes	
Basic Circuit Analysis	
Electronic Devices	
Analog Electronic Circuits 433 03 1	

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tools.	Digital Integrated Circuits
tools.	Linear Integrated Circuits
	VLSI Design
	Electronic Design and Automation Lab
	Design of Fault Tolerant System
	Project
PSO2: The Student will be able to develop	Programming in C & C++
preliminary skills and capabilities necessary for	Programming Lab
embedded system design and demonstrate	Pulse Digital And Switching Circuits
understanding of its societal impact.	Digital Integrated Circuits and Applications
	Microprocessor and Microcontroller
	Microprocessor and Microcontroller Lab
	Integrated Circuits lab
	Embedded systems
	RTOS
	Project
PSO 3: The ECE Graduates will be able to obtain	Signal Analysis and Transform Techniques
the knowledge of working principles of modern	Digital Signal Processing
communication systems and be able to develop	DSP lab
simulation models of components of a	Digital Image Processing
communication system.	Mobile Cellular Communications
	Analog and Digital Communication
	Radar and Satellite Communications
	Project
PSO 4: The ECE Graduates will develop soft	English
skills, aptitude and programming skills to be	English Lab
employable in IT sector.	Programming in C & C++
Sample Vinder	Programming Lab
	DSP Lab
	EDA Lab
	Project
	7.03555

Curricular Gaps identified in attainment of POs and PSOs

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Gap 1:As per PO3, the students are exposed to systems design but they are not doing system design taking public health, safety, cultural, societal, and environmental needs into considerations. A clear stress has to be given in this aspect.

Gap 2: As per PO 6, the student is expected to realize his/her responsibilities relevant to the professional engineering practice keeping societal, health, safety, legal, and cultural issues in mind. Only General Seminar, project and Project seminar are the courses that partly cater to this PO. The student needs to be in a better position in this regard.

Gap 3: As per PO8 on Ethics a student is expected to apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice. This PO is covered partially in the final year project and hence there is a clear and evident gap in this area.

**Gap 4:** As per PO11 wherein principles of project management and finance are to be demonstrated by the student in multidisciplinary environments, there are two subjects MEA and IAFM in the curriculum that partially caters to this requirement and is a curricular gap.

Gap 5: As per PSO 1, the student needs to get knowledge of complete design flow in both analog and digital VLSI design areas. It is observed that the curriculum stresses on sub system design and integration of smaller subsystems to form a complete working system and is not focused towards carrying out projects. It is expected that the student has ability to design circuits for complicated system and even generate layouts of Analog and Digital ICs.

**Gap 6:** In PSO 2, the students are well versed with conventional microprocessors(X86 architecture) and microcontrollers (8051 based controllers), however in many advanced applications latest processors and controllers like Arduino, Raspberry pi, ARM, PIC etc are employed. The students need to get a first hand exposure of doing complete embedded system design using these controllers.

Muffakham Jah College Of Engineering & Technology, Banjara Hills, Road No. 3, TYPERSEAD Gap 7: In PSO 3, there is only one laboratory, namely DSP lab in the curriculum that provides the students an opportunity in getting acquainted in the direction of signal processing and communication. More avenues of practical learning are to be created in this direction.

Gap 8: In PSO 4, it is highlighted that the students should acquire soft skills, Aptitude and technical skills to work in software industry. An improvement in general aptitude and programming skills with an introductory knowledge of latest techniques like cloud computing, data analytics etc. would give the students a better chance to crack the campus placement.

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### CO attainment statement

Course	Course Title	Percentage Attainment	Score
Code	Twee	710000000000000000000000000000000000000	
	I year	95.5	9.55
EG 101	English		8.05
MAT 101	Mathematics 1	80.5	8.05
MAT 102	Mathematics 2	80.5	
PH 101	Engineering Physics	69.5	6.95
CH 101	Engineering Chemistry	81	8.1
CS 101	Programming in C and C++	83	8.3
CE 101	Engineering Mechanics	74.5	7.45
CE 102	Engineering Graphics	78	7.8
PH 132	Physics Lab	97	9.7
CH 132	Chemistry Lab	87.5	8.75
ME 131	Workshop Practice	97.5	9.75
CS 131	Programming Lab c/c++	95	9.5
EG 131	English Language Lab	96.5	9.65
EG 131	II year I Semester		
MAT 201	Mathematics 3	70.5	7.05
	Basic Circuit Analysis	80.5	8.05
EC 201		68	6.8
EC 202	Electromagnetic Theory	81.5	8.15
EC 203	Electronic Devices	77.5	7.75
ME 221	Elements of Mechanical Engineering	77	7.7
EE 222	Electrical Technology	90	9.0
EC 231	Electronic Devices lab	V. 2	9.0
EC 232	Electronic Workshop Lab and Basic	90	9.0
	Circuits Lab		
	II year II Semester		1 22
MAT 251	Mathematics IV	86	8.6
EC 251	Analog Electronics Circuits	81.5	8.15
EC 252	Networks and Transmission Lines	72	7.2
EC: 253	Signal Analysis and Transform Techniques	69	6.9
EC 254	Pulse Digital and Switching circuits	75.5	1 7:55

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CE 222	Environmental Sciences	89.5	8.95
EC 281	Electronic Circuits Lab	85.5	8.55
EE 292	Electrical Technology Lab	85.5	8.55
	III year I Semester		
EC 301	Linear Integrated Circuits and Applications	68	6.8
EC 302	Digital Integrated Circuits and Applications	74	7.4
EC 303	Analog Communications	72.5	7.25
EC 304	Automatic Control Systems	77.5	7.75
EC 305	Microprocessors and Microcontrollers	62.5	6.25
EC 331	Integrated Circuits Lab	82	8.2
EC 332	Analog Communication Lab	80	8.0
EC 333	Microprocessor and Microcontroller Lab	79	7.9
	III year II Semester		
EC 351	Digital Communication	74	7.4
EC 352	Digital Signal Processing	72.5	7.25
EC 353	Antenna and Wave Propagation	77.5	7.75
EC 354	Computer Organisation and Architecture	68	6.8
EC 355	Electronic Instrumentation	78.5	7.85
CM 371	Managerial Economics and Accountancy	86.5	8.65
EC 381	Digital Communication lab	81	8.1
EC 382	Digital Signal Processing Lab	86.5	8.65
EC 383	Industrial Visit/Study	:=	( <b>-</b> ):
	IV year I Semester		
EC 401	Microwave Engineering	80.3	8.03
EC 402	VLSI Design	77	7.7
EC 403	Computer Networking	66.7	6.67
EC 404	Mobile Cellular Communication	67.5	6.75
EC 411	Elective 1: Embedded Systems	88.6	8.86
EC 413	Elective 1: Digital Image Processing	58.4	5.84
ME 472	Industrial Administration and Financial Management	78.5	7.85
EC 431	Microwave Engineering Lab	91.2	9.12

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EC 432	Electronic Design and Automation Lab	81.9	8.19
EC 433	Project Seminar	75.5	7.55
	IV year II Semester		
EC 451	Radar and Satellite Communication	93.7	9.37
EC 463	Elective 2: Design of Fault Tolerant	58.8	5.88
	Systems		
EC 461	Elective 2: Wireless sensor networks	56.25	5.625
EC 472	Elective 3: Global Positioning System	59.4	5.94
EC 473	Elective 3: Neural Networks and Fuzzy	79.6	7.96
	logic		
EC 481	Seminar	86	8.6
EC 482	Project	81.5	8.15

### Analysis of attainment of Course outcomes

Sl.No	Course outcome attainment level	No of Courses at this level	% courses at each level
1	Course outcome Attainment Score > 9.0	9	14.51
2	Course outcome Attainment Score > 8.0	22	35.48
3	Course outcome Attainment Score > 7.0	19	30.64
4	Course outcome Attainment Score > 6.0	8	12.9
5	Course outcome Attainment Score > 5.0	4	6.45
	Total number of courses	62	
	CO Attainment Threshold fixed by ECE department	7.0	

CO Attainment Threshold fixed by ECE department = 7.0

Number of courses having CO attainment more than department threshold = 50 out of 62

Number of courses having CO attainment less than department threshold = 12 out of 62

Note: It is observed that 80.6% of the courses are above the department threshold and hence the department is claiming 4\*8.06 = 32.24 marks out of 40.

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